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Application Number

Filing Date

First Named Inventor

Schott et al.

Art Unit

Examiner Name

Attorney Docket Number

6305-0010WOUS

[illegible]

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ^o
		Country Code ¹ Number * Kind Code ² (if known)				
TC		EP 0 362 493 A1*	07-11-89	Landis & Gyr Be	*U.S. EQUIVALENT	✓
TC		* US 5057890				

/Tsz Chiu/ (02/05/2007)

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STATEMENT BY APPLICANT**

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Filing Date

First Named Inventor

Schott et al.

Art Unit

Examiner Name

Sheet 2

of 2

Attorney Docket Number

6305-0010WOUS

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
TC		FALK U: "A symmetrical vertical Hall-effect device", SENSORS AND ACTUATORS A (PHYSICAL), Bd. A22, Nr. 1-3, MARCH 1990 (1990-03), pages 751-753, XP000358526 ISSN: 0924-4247	
TC		ROUMENIN C S: "Parallel-field Hall microsenors: an overview", SENSORS AND ACTUATORS A (PHYSICAL), Bd. A30, No. 1-2 January 1992 (1992-01), pages 77-87, XP000277696 ISSN: 0924-4247	
TC		SCHURIG E et al.: "CMOS Integrated vertical Hall sensor with low offset", PROCEEDINGS EUROSensors XVI, 16TH EUROPEAN CONFERENCE ON SOLID-STATE TRANSDUCERS, Prague, Czech Republic, 15.-18. September 2002, pages 868-871, XP002241233	
TC		STEINER R et al. "Double-Hall sensor with self-compensated offset", INTERNATIONAL ELECTRON DEVICES MEETING, IEDM 97, Washington, D.C. USA, 7.-10. Dezember pages 911-914, XP000855940 ISBN: 0-7803-4100-7	
TC		SCHURIG E et al: "A vertical Hall device in CMOS integrated high-voltage technology", SENSORS AND ACTUATORS A (PHYSICAL), 97-98 (2002) 47-53	

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